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Reactive Power Injection Strategies for Single-Phase Photovoltaic Systems Considering Grid Requirements

Yongheng Yang, *Student Member, IEEE*, Huai Wang, *Member, IEEE*, and Frede Blaabjerg, *Fellow, IEEE*

Abstract—As the development and installation of photovoltaic (PV) systems are still growing at an exceptionally rapid pace, relevant grid integration policies are going to change consequently in order to accept more PV systems in the grid. The next generation PV systems will play an even more active role like what the conventional power plants do today in the grid regulation participation. Requirements of ancillary services like Low-Voltage Ride-Through (LVRT) associated with reactive current injection and voltage support through reactive power control, have been in effectiveness in some countries, e.g. Germany and Italy. Those advanced features can be provided by next-generation PV systems, and will be enhanced in the future to ensure an even efficient and reliable utilization of PV systems. In light of this, Reactive Power Injection (RPI) strategies for single-phase PV systems are explored in this paper. The RPI possibilities are: a) constant average active power control, b) constant active current control, c) constant peak current control and d) thermal optimized control strategy. All those strategies comply with the currently active grid codes, but are with different objectives. The proposed RPI strategies are demonstrated firstly by simulations and also tested experimentally on a 1 kW single-phase grid-connected system in LVRT operation mode. Those results show the effectiveness and feasibilities of the proposed strategies with reactive power control during LVRT operation. The design and implementation considerations for the characterized RPI strategies are also discussed.

Index Terms—Reactive power injection, single-phase systems, photovoltaic (PV) systems, grid requirements, low-voltage ride-through (LVRT), power (PQ) control, junction temperature, reliability

I. INTRODUCTION

THE ADVANCEMENTS of power electronics technologies have shown great potential for renewable energy integration into the grid, as proved by the continuously booming penetration level of PhotoVoltaic (PV) systems [1]–[5], which leads to increased grid decentralization and vulnerability. Hence, catering for further more PV installations calls for advanced control strategies in compliance with grid

requirements or standards. Currently, it is required that the PV systems cease to energize local loads in the presence of grid abnormal conditions, e.g. voltage sags and frequency variations [6]–[11]. Meanwhile, it is required in those grid regulations for most systems to operate at unity power factor (or a minimum power factor, e.g. power factor ≥ 0.9) with Maximum Power Point Tracking (MPPT) control in order to extract as much energy as possible from the PV panels [11]–[14]. Those grid requirements are valid in case of a low penetration degree of PV systems, including the most commonly used single-phase systems.

However, a still increasing adoption of PV systems will violate the grid integration. For example, potential overloading or voltage rises may appear at distributed grid feeders, especially when a very high penetration level of PV systems is reached, due to the intermittent nature of solar PV source and the unbalance between PV supply and load demands [2], [4], [10], [15]–[21]. Possibilities to solve those issues include limiting feed-in maximum power from PV systems [22] and reducing installations, which are against the goal of carbon reduction in most countries, e.g. Germany, by enabling an even more wide-scale adoption of renewable energies. Thus, those countries have put forward specific grid requirements for large-scale PV systems, which should be able to participate in voltage regulation through reactive power control (injecting or absorbing reactive power), as static grid support [23]–[26].

Meanwhile, the trip-off of an aggregated PV system owing to anti-islanding protection may induce grid variations, leading to more serious events, e.g. power outage [4], [10], [21], [27]–[29]. Hence, in response to grid disturbances, it is better for next-generation PV systems to provide dynamic grid support in terms of Low-Voltage Ride-Through (LVRT) with Reactive Power Injection (RPI), in order to: a) stabilize the grid in case of failures and b) to avoid loss of massive PV generation systems. For instance, in Italy, any generation system with the total power exceeding 6 kW should have LVRT capability [24]. In Germany, it has been in effectiveness for medium- or high-voltage systems, including grid-connected PV systems [4], [28]–[32]. Other countries also keep the pace with grid code revisions [10], [33]–[35]. Those requirements were firstly introduced to wind turbine systems, but today tend to be extended to all PV systems, even for PV modules [34]. Obviously, the implementation of LVRT function violates the anti-islanding requirement. Hence, as it is shown in Fig. 1, compatibility of those two functions should be taken into considerations when upgrading grid requirements.

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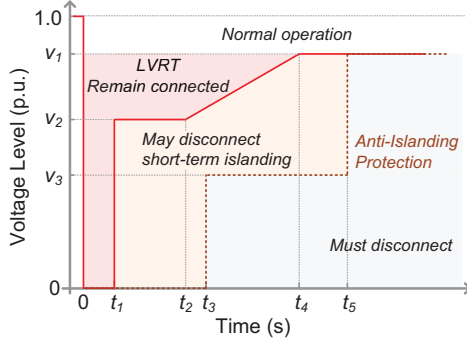


Fig. 1. Suggestion on a compatible implementation of low-voltage (and zero-voltage) ride-through and anti-islanding requirements for single-phase PV systems connected to low-voltage networks.

Nonetheless, as the penetration level is continuously growing, much controllability of active power and reactive power should be ensured in future PV systems, including reactive power control to support the grid voltage statically (voltage rise mitigation) and also ride-through faults dynamically, which is associated with RPI control during the transients. In light of the above issues, this paper explores single-phase RPI strategies, including: a) constant average active power control, b) constant active current control, c) constant peak current control and d) thermal optimized reactive power control strategy. Firstly, a brief introduction of the power control for single-phase PV systems is given in § II, followed by the proposed RPI methods. Simulations and experiments were carried out on a 1 kW single-phase system in the LVRT operation mode and presented in § IV. Both results have verified the effectiveness of the proposed RPI strategies.

II. POWER CONTROL OF SINGLE-PHASE SYSTEMS

Since the PV systems are still dominantly for residential applications at present, single-phase topologies are more widely-used solutions for PV systems. Fig. 2 represents a typical single-phase grid-connected PV system, where, in some cases, a DC-DC converter is adopted to boost up the PV panel voltage within an acceptable range of the PV inverter [6], [9], [11]. It also offers the flexibility of MPPT control, which is a basic requirement for such systems operating at unity power factor. Meanwhile, the injected current should be synchronized with the grid voltage, and as mentioned previously, the system should disconnect from the grid when it presents disturbances (e.g. frequency or voltage variation) at the Point of Common Coupling (PCC) as shown in Fig. 2.

As for the control of single-phase systems with the RPI function, the droop control concept [8], [36] for single-phase PV systems is not suitable, since it requires that the line is mainly inductive (i.e. $X \gg R$). The utilization of adaptive filtering technique leads to an instantaneous power control solution [37]. This power control method is a good candidate for single-phase systems when a satisfactory synthesis of the power references is achieved. Besides the above possibilities, the power control can also be developed in the dq - or $\alpha\beta$ -frame, based on the single-phase PQ theory [11], [37]–[42]. The implementation of this control solution is intuitive

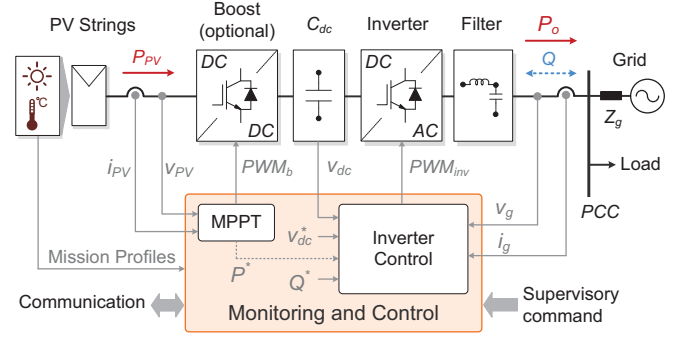


Fig. 2. Typical power and control configuration of a single-phase grid-connected PV system.

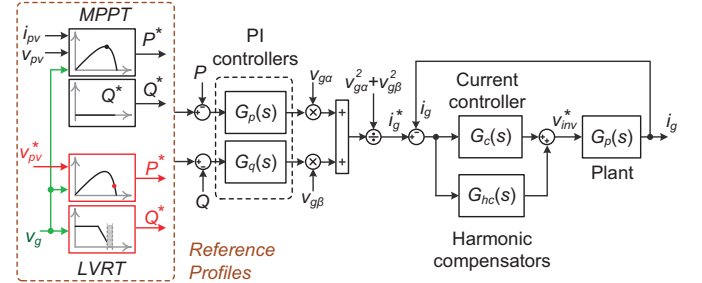


Fig. 3. Control structure in the $\alpha\beta$ -frame for single-phase single-stage PV systems based on single-phase PQ theory [11], [40].

with less complexity, but it requires an Orthogonal Signal Generation (OSG) system to create quadrature components ($v_{g\alpha}$, $v_{g\beta}$ and $i_{g\alpha}$, $i_{g\beta}$) corresponding to the real grid voltage v_g and current i_g , as shown in Fig. 3. Moreover, a power calculation method in terms of fast computation and high accuracy can contribute to the control performance.

Thus, the RPI control can be implemented in this control solution as the “Reference Profiles” by setting the references for active power P^* and reactive power Q^* , and then the grid current reference i_g^* is generated. In normal operation mode, the active power reference P^* is the tracked maximum power, P_{MPP} , of the PV panels ($P^* = P_{MPP}$) and $Q^* = 0$ Var. When the RPI control is enabled by a detected grid condition (voltage and frequency range), the reactive power is injected according to the grid requirements, e.g. the German grid code shown in Fig. 4(a) [4], [29]. It is noted that, during fault ride-through operation, the system should inject sufficient reactive current according to the grid voltage level [4], [29], [31]. This relationship can be defined as,

$$k = \frac{(I_q - I_{q0})/I_N}{(1 - v_g)}, \text{ when } I_q < I_N \quad (1)$$

where I_{q0} is the initial reactive current before grid failure, and v_g is the instantaneous voltage in p.u. during voltage fault. Since the PV systems are required to operate at unity power factor in MPPT mode, there is no reactive power injection before voltage sags, i.e. $I_{q0} = 0$ A. Moreover, it is required by this grid code that k should be larger than 2 p.u., i.e. $k \geq 2$ p.u., for a minimum reactive current injection [29]. For example, when the grid voltage sags to 0.8 p.u., a minimum reactive current I_q (40 % of the rated current I_N) should be

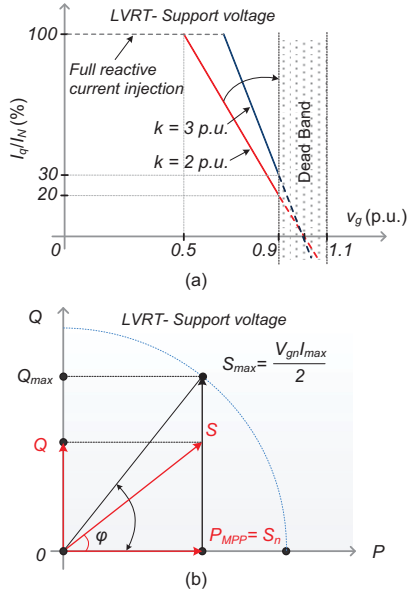


Fig. 4. Reactive power profiles for single-phase systems: (a) during LVRT for medium- and/or high-voltage systems [29], [31], [43] and (b) reactive power capability of a PV inverter.

injected into the grid. It is also shown in Fig. 4(a) that under a severe voltage fault (e.g. $v_g = 0.3$ p.u.) full reactive power injection should be enabled, where the active power injection could be deactivated. However, the amount of reactive power is limited by the inverter apparent power, S_{max} , as it is illustrated in Fig. 4(b). This constraint should be taken into account when designing the RPI strategies, i.e. the avoidance of inverter trip-off due to over-current protection.

III. REACTIVE POWER INJECTION STRATEGIES

Some grid specifications have been imposed on the next-generation PV systems, especially for medium-/high-voltage applications [26], [27]. In the future, PV systems, covering a wide range of applications, have to provide reactive power under grid faults. As discussed in the last paragraph, when designing the RPI control strategies, both the grid requirements (e.g. Fig. 4(a)) and the inverter current limitation shown in Fig. 4(b) have to be complied. Those constraints are given as,

$$\begin{cases} I_q = I_N, & 0 \leq v_g < (1 - \frac{1}{k}) \text{ p.u.} \\ I_q = k(1 - v_g)I_N, & (1 - \frac{1}{k}) \text{ p.u.} \leq v_g < 0.9 \text{ p.u.} \end{cases} \quad (2)$$

where $k \geq 2$ p.u. is defined in (1), and

$$I_{gmax} = \sqrt{I_d^2 + I_q^2} \leq I_{max} \quad (3)$$

in which I_d is the active current, I_{gmax} is the amplitude of the injected current, and I_{max} is the inverter allowable maximum current level. In accordance with (2) and (3), the following RPI strategies are proposed:

A. Constant Average Active Power Control (Const.-P)

The objective of this RPI control strategy is to maximize the output energy with MPPT control during LVRT operation. Therefore, the average active power is maintained constant in

the short-term period. Based on the single-phase PQ theory, the average active power can be given as,

$$P = \frac{1}{2} v_{gm} I_d \quad (4)$$

where v_{gm} is the amplitude of the grid voltage during MPPT operation and I_d is the active current of the injected grid current. In the normal operation mode, $I_d = I_N$, and hence, under LVRT situation with *Const.-P* control, the average active power $P = k_d P_N = \frac{k_d}{2} v_{gmn} I_N$, with v_{gmn} , I_N being the nominal values of the grid voltage and current, respectively, and k_d being the power derating factor.

According to (2) and (4), when the instantaneous grid voltage level v_g : $(1 - \frac{1}{k}) \text{ p.u.} \leq v_g < 0.9 \text{ p.u.}$, the current in the dq -frame can be expressed as,

$$\begin{cases} I_d = \frac{k_d}{v_g} I_N \\ I_q = k(1 - v_g) I_N \end{cases} \quad (5)$$

in which k is defined in (1), and k_d has been given previously. When the grid voltage level sags to lower than $(1 - \frac{1}{k}) \text{ p.u.}$ (i.e. a severe voltage sag occurs), the system is required to fully inject reactive power while the active power output may be disabled (i.e. $I_q = I_N$). In that case, the system might still operate at *Const.-P* mode, depending on the inverter current limitation, and the current in the dq -frame is given by,

$$\begin{cases} I_d = \frac{k_d}{v_g} I_N \\ I_q = I_N \end{cases} \quad (6)$$

However, when the required injection of reactive power is fulfilled, according to (3), it might pose the inverter at a risk of over-current and thus over-heating with this control strategy to maintain a constant output power (i.e. maximum power with MPPT control). Thus, based on (5) and (6), the following constraints should be satisfied in order to avoid inverter shutdown during LVRT:

$$\frac{1}{v_g} \sqrt{k_d^2 + k^2(v_g - v_g^2)^2} \leq \frac{I_{max}}{I_N}, \quad (7)$$

when $(1 - \frac{1}{k}) \text{ p.u.} \leq v_g < 0.9 \text{ p.u.}$, and

$$\frac{1}{v_g} \sqrt{k_d^2 + v_g^2} \leq \frac{I_{max}}{I_N} \quad (8)$$

when $v_g < (1 - \frac{1}{k}) \text{ p.u.}$. Those could be the design criterions for component selection, and can be illustrated in Fig. 5.

It is observed in Fig. 5 that the minimum value of the inverter current limitation (I_{max}) should be $2.24 I_N$ when $k = 2$ p.u. so that the RPI strategy can be adopted in case of a wide range of voltage drop (i.e. the grid voltage is within $0.5 \text{ p.u.} \leq v_g < 0.9 \text{ p.u.}$) without power derating ($k_d = 1$ p.u.). As for a predesigned PV inverter with a robustness margin, the system has to derate the output power in order to inject enough reactive power. For example, if the allowable maximum current of a PV inverter, $I_{max} = 1.5 I_N$ and $k = 2$ p.u., the PV systems should reduce the active power output (e.g. $k_d = 0.5$ p.u.), when the voltage drops below 0.72 p.u. , as it is shown in Fig. 5. It is also noted in Fig. 5 that, by derating operation, the *Const.-P* strategy can be adopted for a wider range of voltage sags.

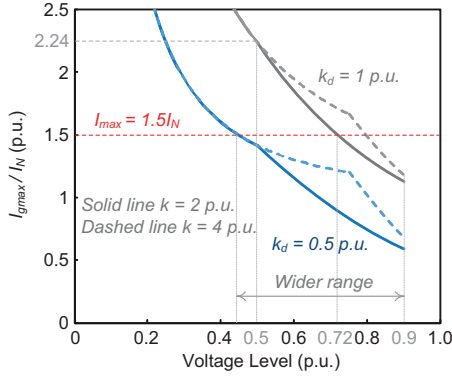


Fig. 5. Design constraint of the *Const.-P* considering the inverter over-current protection, where $k_d = P/P_N$ and k is defined in (1).

B. Constant Active Current Control (*Const.-I_d*)

Another RPI control possibility under LVRT operation is to keep the active current constant (i.e. $I_d = \text{const.}$). According to (4), the active current I_d can be obtained as,

$$I_d = \frac{2P}{v_{gm}} = mI_N = \text{const.} \quad (9)$$

in which m is defined as the scaling factor for the design consideration in case of derating operations, and $0 \leq m \leq 1$ p.u.. According to (9), the active power will automatically be reduced when this RPI control strategy is adopted in the response to voltage sags, i.e. $P \propto v_{gm}$. Meanwhile, the reactive current I_q can be calculated according to the requirement shown in Fig. 4(a) and (2). Subsequently, the current in the dq -frame can be given as,

$$\begin{cases} I_d = mI_N \\ I_q = k(1 - v_g)I_N \end{cases} \quad (10)$$

where $(1 - \frac{1}{k})$ p.u. $\leq v_g < 0.9$ p.u. and k are defined previously. Notably, when a severe voltage fault happens (very low voltage), the PV system should inject full reactive power. In that case, the current in dq -frame can be expressed as,

$$\begin{cases} I_d = mI_N \\ I_q = I_N \end{cases} \quad (11)$$

when $v_g < (1 - \frac{1}{k})$ p.u..

With the *Const.-I_d* control strategy, the amplitude of the injected current may also exceed the inverter limitation according to (3), and then trip the inverter protection. In order to avoid this, the following conditions should be fulfilled,

$$\sqrt{m^2 + k^2(1 - v_g)^2} \leq \frac{I_{max}}{I_N}, \quad (12)$$

when $(1 - \frac{1}{k})$ p.u. $\leq v_g < 0.9$ p.u., and

$$\sqrt{m^2 + 1} \leq \frac{I_{max}}{I_N}, \quad (13)$$

when $v_g < (1 - \frac{1}{k})$ p.u.. For simplicity, the level of active current can be controlled to be that of the rated current (i.e. $m = 1$ p.u., $I_d = I_N$).

Similarly, a design guide for this RPI control strategy can be given in Fig. 6. It is seen from Fig. 5 and Fig. 6 that the PV

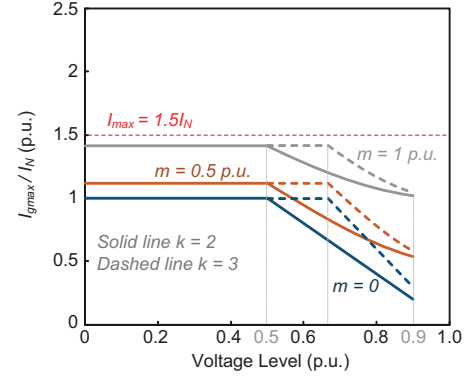


Fig. 6. Design constraint of the *Const.-I_d* considering the inverter over-current protection, where $m = I_d/I_N$ and k is defined in (1).

inverter with *Const.-I_d* control can be designed with a lower I_{max}/I_N when it is compared to the one with *Const.-P* control strategy. Therefore, it offers the possibility to select power devices with lower current ratings and thus lower cost. It is also worth to point out that derating operation of a PV system can be achieved by changing m because of the proportional relationship between the active power and the grid voltage amplitude, $P \propto v_{gm}$. A smaller m leads to the possibility to select power devices of further lower ratings.

C. Constant Peak Current Control (*Const.-I_{gmax}*)

A PV inverter with the previous discussed RPI strategies has a risk of over-current loading when it is operating in LVRT mode. Thus, the *Const.-I_{gmax}* is proposed. With this control strategy, there is no unintentional inverter shutdown due to over-current protection, since the peak of the injected grid current is kept constant and lower than the inverter current limitation during LVRT, i.e. $I_{gmax} = nI_N = \text{const.}$, and $I_{gmax} \leq I_{max}$, where n is defined as the peak current scaling factor. According to (2), when the grid voltage is within the range: $(1 - \frac{1}{k})$ p.u. $\leq v_g < 0.9$ p.u., the current in dq -frame can be given by,

$$\begin{cases} I_d = \sqrt{n^2 - k^2(1 - v_g)^2} I_N \\ I_q = k(1 - v_g)I_N \end{cases} \quad (14)$$

while, if the grid voltage goes further lower than $(1 - \frac{1}{k})$ p.u., according to (3) the current in dq -frame should be,

$$\begin{cases} I_d = \sqrt{n^2 - 1} I_N \\ I_q = I_N \end{cases} \quad (15)$$

where v_g and k are defined previously.

It should be noted that n has a maximum value of $\frac{I_{max}}{I_N}$ p.u. considering inverter current protection shown in (3). For example, when a inverter is designed with a margin of 2 p.u. (i.e. $I_{max} = 2I_N$), the maximum n should be 2 p.u. to ensure a stable RPI without tripping the inverter during LVRT. Thus, if $n \leq \frac{I_{max}}{I_N}$, riding-through operation of the PV inverter will not give an amplitude rise to the injected grid current. Meanwhile, according to (4) and (14), the active power will be reduced in order to inject sufficient reactive power during LVRT.

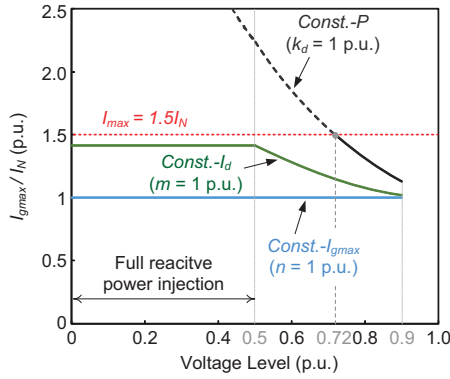


Fig. 7. Application conditions for three RPI strategies under different grid voltage levels for the case when $k = 2$ p.u. and $I_{max} = 1.5 I_N$.

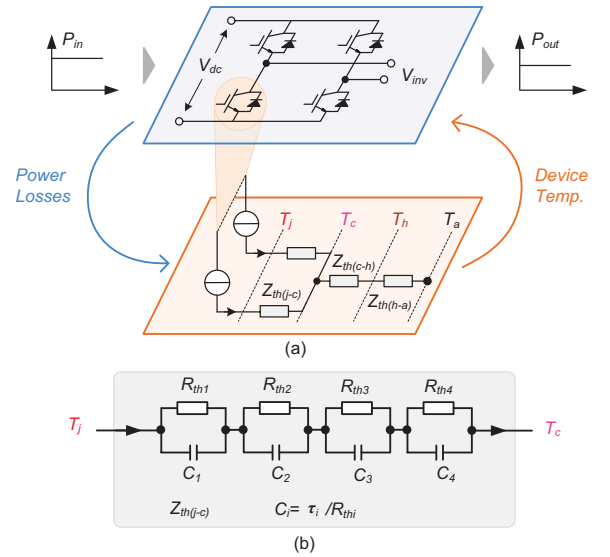
As aforementioned, the application conditions of the three proposed RPI complied with (2) are dependent of several parameters - k , k_d , m , n , and I_{max}/I_N . Fig. 7 shows an comparison of three RPI strategies for a PV inverter with $I_{max} = 1.5 I_N$ when $k = 2$ p.u.. It can be observed in Fig. 7 that the *Const.-I_{gmax}* can be used in a wide range of voltage levels ($0 \leq v_g < 0.9$ p.u.) even when k is different. This is the same case when the *Const.-I_d* is adopted, as also proved by Fig. 6. In contrast, the *Const.-P* is only in effectiveness within a certain range of grid voltage levels, if without power derating operations ($k_d = 1$ p.u.). For example, when the voltage level goes below 0.72 p.u. in the exemplified PV inverter, the RPI strategy should be changed to either *Const.-I_d* or *Const.-I_{gmax}*, or derating operations have to be enabled as shown in Fig. 5, and otherwise the inverter will be tripped off due to over-current protection. Notably, as shown in Fig. 7, all three RPI strategies have to inject full reactive current in case of a severe voltage sag (e.g. $v_g < (1 - \frac{1}{k})$ p.u.) according to (2) and Fig. 4(a).

D. Thermal Optimized Control Strategy (T-Optimized)

High efficiency and high reliability have become of intense importance for next-generation PV inverters in order to reduce the cost of energy [10], [44]–[46]. In respect to reliability, possibilities to improve it can be achieved by considering rated power, packaging technologies, severe users, and harsh operation conditions, e.g. under grid faults [44]–[47]. However, as exemplified in a model of (16), the junction temperatures, including mean junction temperature, T_{j-m} , and temperature swings, ΔT_j , have a significant impact on the number of cycles to failure N_f of a power device [48].

$$N_f = \alpha (\Delta T_j)^{\beta_1} e^{\frac{\beta_2}{T_{j-m}}} (t_{ON})^{\beta_3} \cdot (other factors), \quad (16)$$

with t_{ON} being the pulse length, and α , $\beta_{1,2,3}$ being the coefficients related to the device material, which can be obtained by means of curve-fitting using numerical simulation or experimental accelerating tests [49]. According to the Miner's rules [44], [45], [48]–[50], the power device lifetime is linearly dependent on temperature stress damage (i.e. N_f). Calculations of N_f based on the temperature profiles have been presented in [44] and [45] in details. Those studies have



Notes: T_j – junction temperature $Z_{th(j-c)}$ – junction-to-case impedance
 T_c – case temperature $Z_{th(c-h)}$ – case-to-heatsink impedance
 T_h – heatsink temperature $Z_{th(h-a)}$ – heatsink-to-ambient impedance
 T_a – ambient temperature

Fig. 8. (a) Electrical and thermal models of a power module and (b) Foster model of the thermal impedance from junction to case [47], [51], [52].

shown that the junction temperature affects the entire system reliability. Since the thermal behavior of the power devices is coupled with the power losses as it is shown in Fig. 8, appropriate allocation of the active power and reactive power offers the controllability of the junction temperature.

For example, in LVRT operation, the injected reactive power is dependent on the voltage sag level, and also as shown in (2) and Fig. 4(a), k is variable. Both will lead to a redistribution of power losses on the power devices, and also the thermal distribution. Therefore, a constant junction temperature (or at least cooled-down junction temperature) of the power devices, and thus improved overall reliability can be achieved by changing the RPI strategies and/or the slope k shown in Fig. 4(a). For instance, as shown in Fig. 9, a 0.3 p.u. voltage sag (i.e. $v_g = 0.7$ p.u.) occurs and the *Const.-I_{gmax}* is firstly activated. By adjusting the value k to 3 p.u., the operation points will change from **C** to **D**; while by changing the RPI strategy to *Const.-P*, the operation point will correspondingly move from **C** to **A**.

This is the operation principle of the *T-optimized* control strategy in LVRT applications. As for the implementation of *T-optimized* control strategy, one essential part is to create the power reference unit according to voltage sag depths. One method is based on the mathematical derivations, and an alternative is based on look-up tables, which has been adopted in this paper. The look-up table based power reference model can simply be given as,

$$T_{jmax} = f(P_j, Q_j). \quad (17)$$

A detailed implementation of this control strategy is shown in Fig. 10, which implies that the *T-optimized* strategy complies with both RPI requirement in LVRT (performed by “Grid

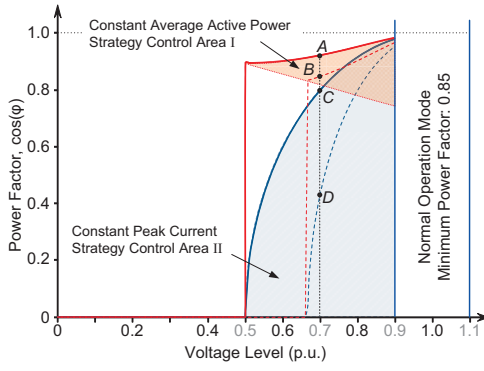


Fig. 9. Power factor curves vs. voltage levels for different control strategies: solid lines: $k = 2$, dashed lines: $k = 3$, and k is defined in (1).

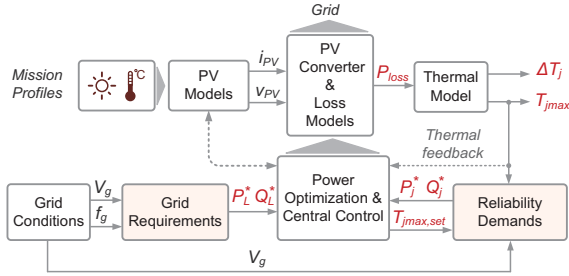


Fig. 10. Control structure of the *T-Optimized* strategy for single-phase PV systems.

Requirements” unit) and improved reliability demand (performed by “*Reliability Demands*” unit). In normal operation mode, since a minimum power factor is required, the system directly sets the references ($P^* = P_{MPP}$ and $Q^* = 0$ Var) for the central control unit, as it is shown in Fig. 11. While in the case of a voltage sag both control units will send out the power references (P_L^* and Q_L^* , P_j^* and Q_j^*), and then central control unit will optimize the power to achieve both goals. Notably, as shown in Fig. 11, in order to achieve a reduced or constant junction temperature, there might be several sets of power references as long as the power losses are reduced or kept constant, according to Fig. 8. In that case, optimization objectives, e.g. $P^* = \max\{P_1^*, P_2^*, \dots\}$, can be applied to determine the power references for the central control unit.

In terms of application conditions for the *T-optimized* control strategy, it is mainly dependent on the maximum junction temperature reference, $T_{jmax,set}$. On the assumption that the system is operating at nominal conditions before failure and the maximum junction temperature is $T_{jmax,0}$. If the grid voltage sags to lower than $(1 - \frac{1}{k})$ p.u., the *T-optimized* control strategy can be applied to realize a controllable junction temperature with $T_{jmax,set} \geq T_{jmax,0}$, while a reduced junction temperature (i.e. $T_{jmax,set} < T_{jmax,0}$) can not be achieved. When the grid voltage is within the range: $(1 - \frac{1}{k})$ p.u. $\leq v_g < 0.9$ p.u., the junction temperature can be controlled either lower than that before failure (i.e. $T_{jmax,set} < T_{jmax,0}$) or at a certain level (e.g. $T_{jmax,set} = T_{jmax,0}$), depending on the voltage sag depth. It should be noted that, in case of over-current protection (or over-heating protection), the temperature reference should satisfy this condition: $T_{jmax,set} \geq T_{jmax,inv}$,

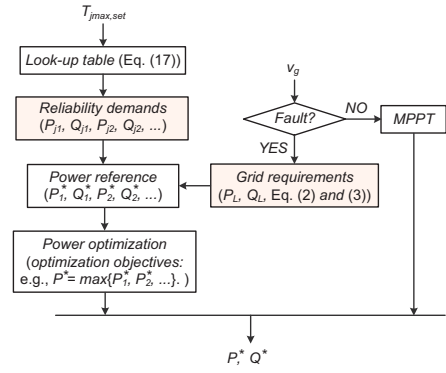


Fig. 11. Flowchart of the *T-optimized* control strategy.

with $T_{jmax,inv}$ being the allowable maximum junction temperature of the power devices.

However, it should be pointed out that a voltage fault normally is a very short-term event, and thus the *T-optimized* control strategy may not take a fast and effective response to the voltage sag during this time interval. Yet, on one hand, the idea of thermal optimization by reallocating the active power and reactive power can be adopted in the power electronics based systems in order to achieve improved reliability, and thus a reduced cost of energy, especially if a wide range of reactive power injection is allowed by future grid codes. On the other hand, the *T-optimized* strategy is also designed to limit the maximum junction temperature, since catastrophic failures may occur due to a short-term single-event (e.g. LVRT) of over-temperature, and thus over-heating, which exceeds the limit of the IGBT power devices.

E. Benchmarking of the Proposed RPI Strategies

During the design and the operation of the PV inverters, those above constraints should be considered. Especially, for the next generation PV systems, the provision of reactive power as an advanced feature both in normal operation and under grid faults, and the requirements of LVRT will come into force in the near future. The corresponding active and reactive power references under different voltage sag levels for the proposed RPI control strategies can be obtained based on the above discussions. Thus, the required reactive power complying with grid codes can be injected. A benchmarking of the proposed RPI strategies is summarized in TABLE I, which shows the advantages and design constraints/considerations of each RPI control strategy.

It can be seen from the benchmarking results that those RPI strategies can fulfill the grid requirements but with different control objectives. The first two strategies are designed with the purpose to produce as much energy as possible during LVRT. Moreover, the *Const.-I_d* control strategy requires a lower power rating for the inverter compared to the *Const.-P* strategy. While the *Const.-I_{gmax}* maintains a constant peak current, and thus it can be implemented in any predesigned PV inverter. The *T-Optimized* strategy is a hybrid alternative considering the RPI and reliability requirements for PV inverters. According to TABLE I, a combination of the proposed

TABLE I
BECHMARKING OF THE PROPOSED REACTIVE POWER INJECTION STRATEGIES.

| RPI Strategy | Advantages | Design Constraints | Remarks |
|--------------------------------|--|--|--|
| <i>Const.-P</i> | <ul style="list-style-type: none"> Maximized energy yield (with MPPT control) | <ul style="list-style-type: none"> Over-current trip-off Derating operation is necessary under severe faults Large design margin for a wide voltage range | It requires a very large design margin for the power devices, and thus cost increases, especially under deep voltage sags. |
| <i>Const.-I_d</i> | <ul style="list-style-type: none"> Reduced power operation ($P \propto v_g$) | <ul style="list-style-type: none"> Over-current issues May also require power derating operation Small design margin for a wide voltage range | For new-design inverters, it has lower rating requirements for the power devices compared to <i>Const.-P</i> . |
| <i>Const.-I_{gmax}</i> | <ul style="list-style-type: none"> No over-current problems Applicable for all inverters | — | No current amplitude rise. This strategy is applicable for all predesigned PV inverters. |
| <i>T-Optimized</i> | <ul style="list-style-type: none"> Compliance with both LVRT and reliability requirements | <ul style="list-style-type: none"> Increased implementation complexity | It is a hybrid solution between the above three strategies. Improvement of reliability can be achieved by reallocating reactive and active powers. |

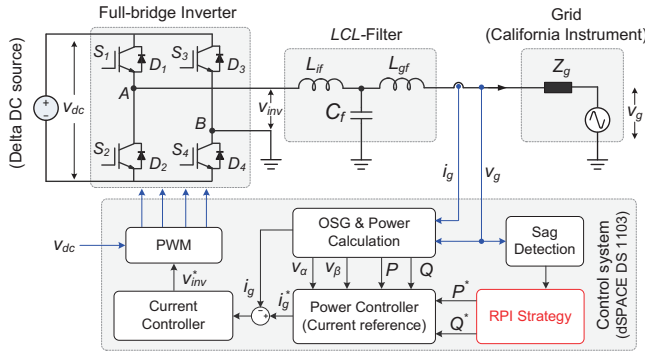


Fig. 12. Hardware schematic and overall control structure of a single-phase grid-connected system with RPI control strategies.

strategies is an inspiring way to cost-effectively design new PV inverters when considering the inverter VA ratings, power production capability, control complexity, and also the occurrence rate of grid voltage sags.

IV. SIMULATION AND EXPERIMENTAL RESULTS

Referring to Fig. 2 and Fig. 3, simulation and experimental tests were carried out to demonstrate the effectiveness of the proposed RPI strategies. Fig. 12 shows the hardware configuration of a single-phase system used for the verifications, and it also shows that the RPI control is triggered by the detected voltage fault (e.g. grid voltage level, v_g). System parameters are listed in TABLE II. In both simulations and experiments, a proportional resonant current controller with harmonic compensators is adopted to maintain a satisfactory power quality, as shown in Fig. 3.

A. Simulation Results

Simulations are firstly carried out, and a voltage sag of 0.45 p.u. is generated (i.e. $v_g = 0.55$ p.u. during LVRT operation). Fig. 13 shows the performance of a 1 kW single-phase system with *Const.-P*, *Const.-I_d*, and *Const.-I_{gmax}* RPI strategies in LVRT. Based on the thermal models in Fig. 8 and thermal parameters of the IGBT module (TABLE III), the same single-phase system with the *T-optimized* RPI control strategy is also simulated and the results are given in Fig. 14 and Fig. 15.

TABLE II
SIMULATION AND EXPERIMENTAL PARAMETERS.

| | |
|----------------------------------|---|
| Nominal grid voltage amplitude | $v_{gmn} = 325.2$ V |
| Nominal grid frequency | $\omega_0 = 2\pi \times 50$ rad/s |
| Grid impedance | $L_g = 4$ mH, $R_g = 0.2$ Ω |
| LCL-filter | $L_{if} = 3.6$ mH, $C_f = 2.35$ μ F, $L_{gf} = 708$ μ H |
| Sampling and switching frequency | $f_s = f_{sw} = 10$ kHz |
| DC voltage | $V_{dc} = 400$ V |

TABLE III
THERMAL PARAMETERS OF THE IGBT MODULE FROM A LEADING MANUFACTURER FOR THE SIMULATIONS.

| Impedance | $Z_{th(j-c)}$, Junction-to-case temp. | | | | |
|-----------|--|--------|-------|-------|-------|
| | | 1 | 2 | 3 | 4 |
| IGBT | R_{thi} (K/W) | 0.074 | 0.173 | 0.526 | 0.527 |
| | τ_i (s) | 0.0005 | 0.005 | 0.05 | 0.2 |
| Diode | R_{thi} (K/W) | 0.123 | 0.264 | 0.594 | 0.468 |
| | τ_i (s) | 0.0005 | 0.005 | 0.05 | 0.2 |

When the voltage sag is detected, the system goes into LVRT operation mode, and the system injects the required reactive power to support the voltage according to Fig. 4(a). At the same time, the active power is also controlled in order to achieve different objectives according to (5), (10) and (14), i.e. to inject the maximum active power during fault ride-through, to keep the active current constant, and to maintain the peak current, as shown in Fig. 13. It can also be observed in Fig. 13(a) that, although the *Const.-P* can maximize the power output during LVRT operation, it also gives a large amplitude rise of the injected current, which may trigger the inverter over-current protection (i.e., fail to ride-through grid faults). This is the same in case that the *Const.-I_d* strategy is adopted in LVRT operation mode as indicated in Fig. 13(b). However, the amplitude increase in *Const.-I_d* controlled system is smaller in contrast with that in the *Const.-P* controlled system. Moreover, since the peak of the injected current is maintained constant during LVRT as shown in Fig. 13(c), the *Const.-I_{gmax}* strategy can operate under a wide range of voltage levels without over-current issues. Those results are in consistency with the previous discussions.

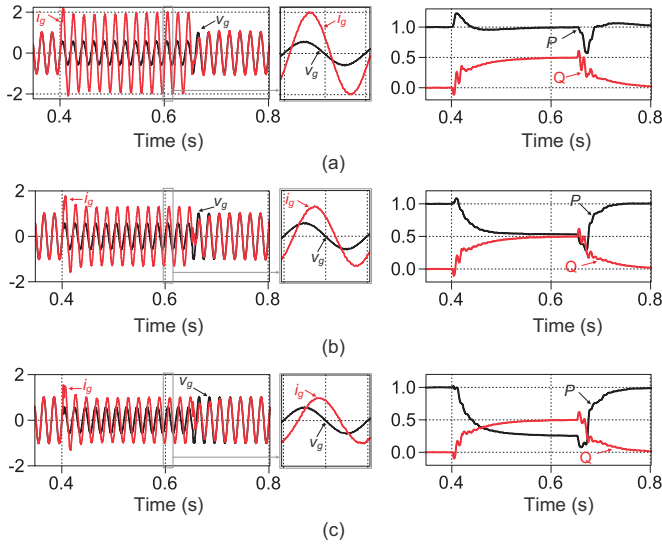


Fig. 13. Performance of a 1 kW single-phase PV system in LVRT operation mode with three different RPI control strategies (i_g , v_g - grid current and voltage [p.u.]; P , Q - average active power and reactive power [p.u.]; voltage level: $v_g = 0.55$ p.u.; $k = 2$ p.u.): (a) *Const.-P* with $k_d = 1$ p.u., (b) *Const.-I_d* with $m = 1$ p.u., and (c) *Const.-I_{gmax}* with $n = 1$ p.u..

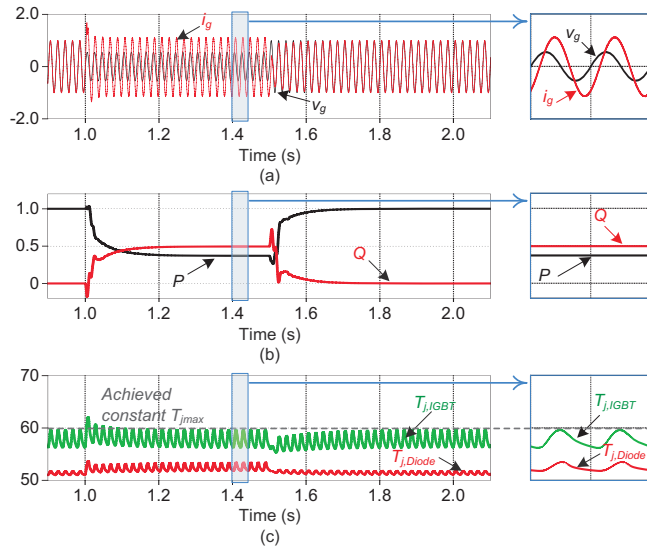


Fig. 14. Simulation results of a 1 kW single-phase grid-connected PV system with *T-optimized* control strategy ($k = 2$ p.u.): (a) grid current and voltage i_g , v_g [p.u.], (b) active and reactive power P , Q [p.u.] and (c) junction temperature T_j [°C]; voltage level: $v_g = 0.55$ p.u..

Notably, with the *T-optimized* control strategy, the junction temperature of IGBT power devices is maintained constant as it is shown in Fig. 14(c) and Fig. 15, while sufficient reactive power is also injected to the grid according to Fig. 4(a). Hence, both required reactive power support and improved reliability are achieved. It is also indicated in Fig. 15 that, the constant maximum junction temperature can be achieved by changing k under grid faults. This is also in accordance with the previous discussions, and viz. (17) may have several solutions (P_{j1} , Q_{j1} , P_{j2} , Q_{j2} , ...). Hence, the optimization objectives should be applied to determine the power references for the control system according to Fig. 11.

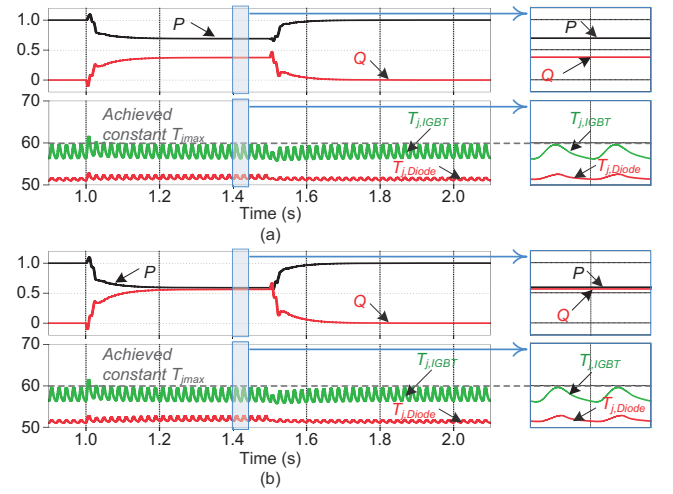


Fig. 15. Performance of a 1 kW single-phase PV inverter with *T-optimized* strategy under grid faults (active and reactive power P , Q [p.u.], junction temperature T_j [°C]; $v_g = 0.75$ p.u.): (a) $k = 2$ p.u. and (b) $k = 3$ p.u..

When comparing the levels of the injected grid current with different control strategies shown in Fig. 13 and Fig. 14(a), it can be found that the *Const.-I_{gmax}* can achieve the lowest current level, and then the junction temperature of the power devices with this RPI strategy is also lower. The analysis has been proved by the results presented in Fig. 16. Since another objective of the *T-optimized* strategy is to maintain a constant maximum junction temperature considering the overall reliability, more active power is injected during fault ride-through as shown in Fig. 14(b). By further reducing the active power injection but providing sufficient reactive power during LVRT, a reduced maximum junction temperature can be achieved, which is another case of the *T-optimized* strategy. This also verified the controllability of junction temperature of the power devices through appropriate allocations of the active power and the reactive power. Nevertheless, it is also observed in Fig. 16 that all the RPI strategies except for the *Const.-P* can limit the maximum junction temperature to some extent, and thus catastrophic failures are avoided.

B. Experimental Tests

In the experimental verifications, a voltage sag of 120 ms has been programmed in an AC power source. The *Const.-P* control strategy has been tested firstly on a 1 kW single-phase grid-connected under a severe voltage sag (0.45 p.u., i.e. $v_g = 0.55$ p.u.). A commercial inverter with the rated current of 5 A in RMS has been used as the conversion stage. If a severe voltage fault (e.g. 0.45 p.u.) happens, the amplitude of the injected grid current may exceed the current limitation, and consequently, the inverter will be tripped off, as it has been verified by the experimental results shown in Fig. 17. One possibility of riding-through fault operation is to reduce the output power (i.e. power derating operation) as discussed previously. Here, in the experimental results shown in Fig. 18, the voltage presents a 0.20 p.u. voltage sag (i.e. $v_g = 0.8$ p.u.) in order to test the performance of *Const.-P* control strategy, and thus the system can operate under this grid fault without

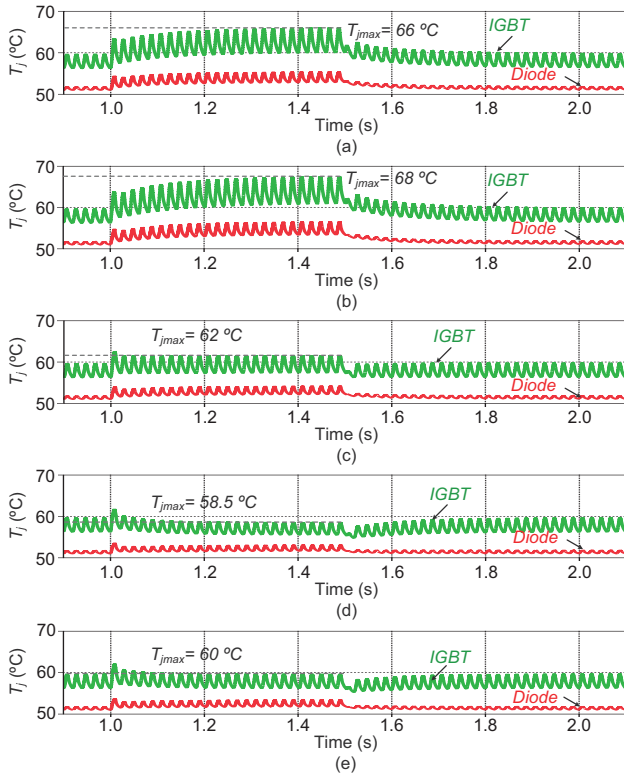


Fig. 16. Junction temperature (T_j) of the power devices in a 1 kW single-phase PV inverter under grid faults (voltage level: $v_g = 0.55$ p.u., $k = 2$ p.u.): (a) without LVRT, (b) *Const.-P* with $k_d = 1$ p.u., (c) *Const.-I_d* with $m = 1$ p.u., (d) *Const.-I_{gmmax}* with $n = 1$ p.u., and (e) *T-optimized*.

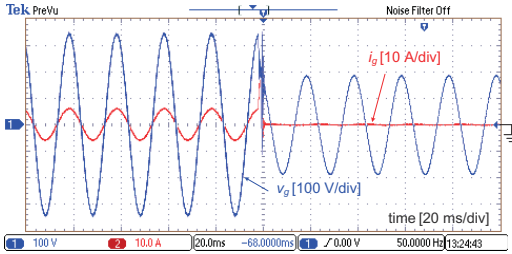


Fig. 17. Over-current protection of a single-phase inverter with *Const.-P* control (voltage level: $v_g = 0.55$ p.u., $k_d = 1$ p.u., and $k = 2$ p.u.).

derating active power. At the same time, sufficient reactive power is injected to the grid according to Fig. 4(a).

In contrast with the simulation results, the *Const.-I_{gmmax}* and *Const.-I_d* control strategies are tested under the severe voltage sag (0.45 p.u.) on the same single-phase grid-connected system. The performance of the system under such a voltage fault is shown in Fig. 19. The results demonstrate that the *Const.-I_{gmmax}* control strategy can contribute to a constant amplitude of the injected current, and thus prevent the inverter from over-current tripping during LVRT. At the same time, an injection of appropriate reactive power according to the requirements shown in 4(a) is ensured by this control strategy. Similarly, the *Const.-I_d* RPI strategy can inject sufficient reactive power, which is dependent on the voltage sag level, and the active current is maintained constant during LVRT, while the injected current amplitude is slightly larger than that in the system

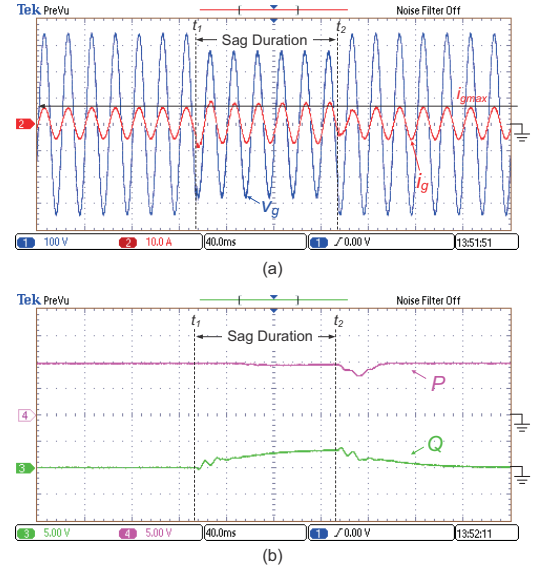


Fig. 18. Experimental results of a single-phase system with the *Const.-P* RPI control strategy (voltage level: $v_g = 0.8$ p.u., $k_d = 1$ p.u., and $k = 2$ p.u.): (a) grid voltage v_g [100 V/div] and grid current i_g [10 A/div]; (b) active power P [500 W/div] and reactive power Q [500 Var/div]; time [40 ms/div].

with *Const.-I_{gmmax}* control. As a consequence, depending on the inverter design margin, this may also trip off the inverter during LVRT. Nonetheless, the above presented results have verified the effectiveness of the proposed RPI strategies for single-phase PV systems as long as the design constraints are taken into account.

A constant power loss before, during and after the LVRT operation could achieve a constant temperature operation. Since the junction temperature measurement under full loading condition is challenging and is still an ongoing research topic in the semiconductor area [53], this paper experimentally measures the power losses of the IGBTs under different control strategies, which can indirectly reveal their thermal stresses. Fig. 20 shows the performance of a single-phase PV system (higher I_{max}/I_N) under a grid voltage sag. It can be seen in Fig. 20 that the *T-Optimized* RPI strategy can inject appropriate reactive power as what the other RPI strategies can do. In contrast, the *T-Optimized* strategy can also achieve a desirable (optimized) junction temperature of the power devices.

This advantage has been indirectly verified by the results shown in Fig. 21, where the power losses are measured using a YOKOGAWA WT3000 Precision Power Analyzer. It can be seen from Fig. 21 that the *T-Optimized* strategy can achieve an almost constant power loss in contrast to that in unity power factor operation (before and after the voltage fault). The *Const.-P* strategy will lead to the highest power loss among those RPI strategies, while the *Const.-I_{gmmax}* will contribute to the lowest. It should be pointed out that, when there is no LVRT control, the power loss will also increase drastically depending on the voltage sag level. According to [44], [45] and [47], the power losses will cause temperature rise at the device junction. This means that, the *Const.-P* and the system without LVRT control might pose the inverter at a risk of over current and also over temperature. The *T-Optimized* control is

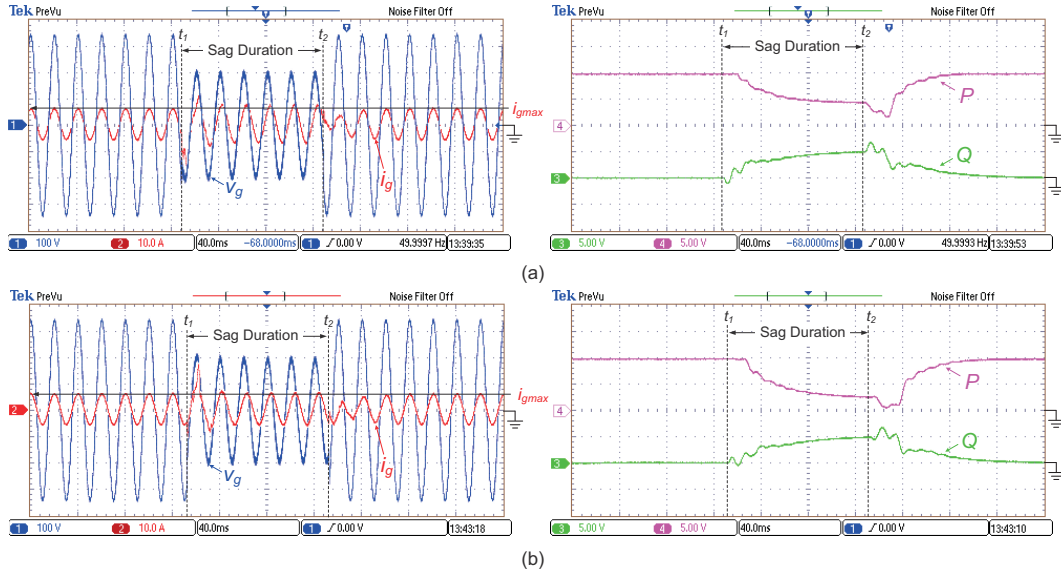


Fig. 19. Experimental results of a single-phase grid-connected system with different RPI control strategies (voltage level: $v_g = 0.55$ p.u., $k = 2$ p.u., grid voltage v_g [100 V/div], grid current i_g [10 A/div], active power P [500 W/div], reactive power Q [500 Var/div], and time [40 ms/div]): (a) *Const.- I_d* with $m = 1$ p.u. and (b) *Const.- I_{gmax}* with $n = 1$ p.u..

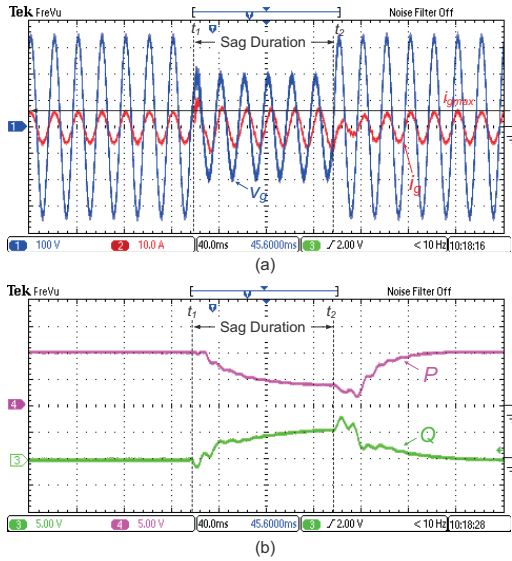


Fig. 20. Experimental results of a 1 kW single-phase system with *T-Optimized* RPI control strategy (voltage level: $v_g = 0.55$ p.u. and $k = 2$ p.u.): (a) grid voltage v_g [100 V/div] and grid current i_g [10 A/div]; (b) active power P [500 W/div] and reactive power Q [500 Var/div]; time [40 ms/div].

able to maintain a constant temperature and the *Const.- I_{gmax}* can achieve the lowest junction temperature. Those results are in agreement with the results shown in Fig. 15, and have corroborated the effectiveness of the proposed RPI strategies in terms of thermal performance.

V. CONCLUSION

In this paper, reactive power injection strategies for single-phase PV systems considering grid requirements have been explored. The proposed reactive power injection strategies include constant average active power control, constant active current control, constant peak current control, and thermal

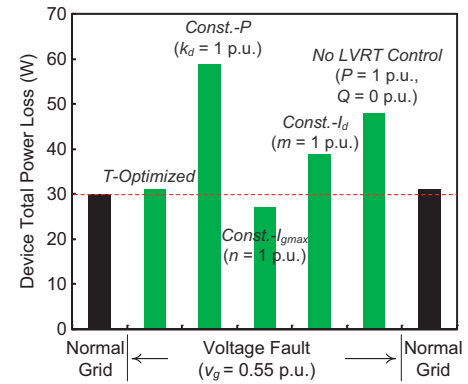


Fig. 21. Total power losses (experiments) of the switching devices in a single-phase system with different RPI control strategies and without LVRT control under a grid fault (voltage level: $v_g = 0.55$ p.u. and $k = 2$ p.u.).

optimized reactive power control strategy, which is dedicated to improve the reliability during LVRT operation and to avoid catastrophic failures. All the discussed control strategies are in compliance with the grid codes currently defined for medium voltage applications. The proposed reactive power control strategies have also been tested either by simulations or by experiments. The results show the effectiveness of the reactive power injection strategies to support the grid voltage during LVRT operation with different objectives, e.g. maximum output power (constant average active power control). Design constraints for those strategies have also been studied in this paper, and a benchmarking of the proposed strategies has been provided. It offers a feasible way to select appropriate power devices for the new PV inverters with the specific control strategy proposed in this paper.

As the future grid demands will be more stringent, and the reactive power injection function is one of them, the PV systems serving even low-voltage grids have to comply

with those requirements in both normal operation mode and under grid faults. The proposed control strategies can be implemented in those PV systems with the provided design guidelines. Hence, the control strategies can further accelerate the pace of advanced PV inverter development.

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